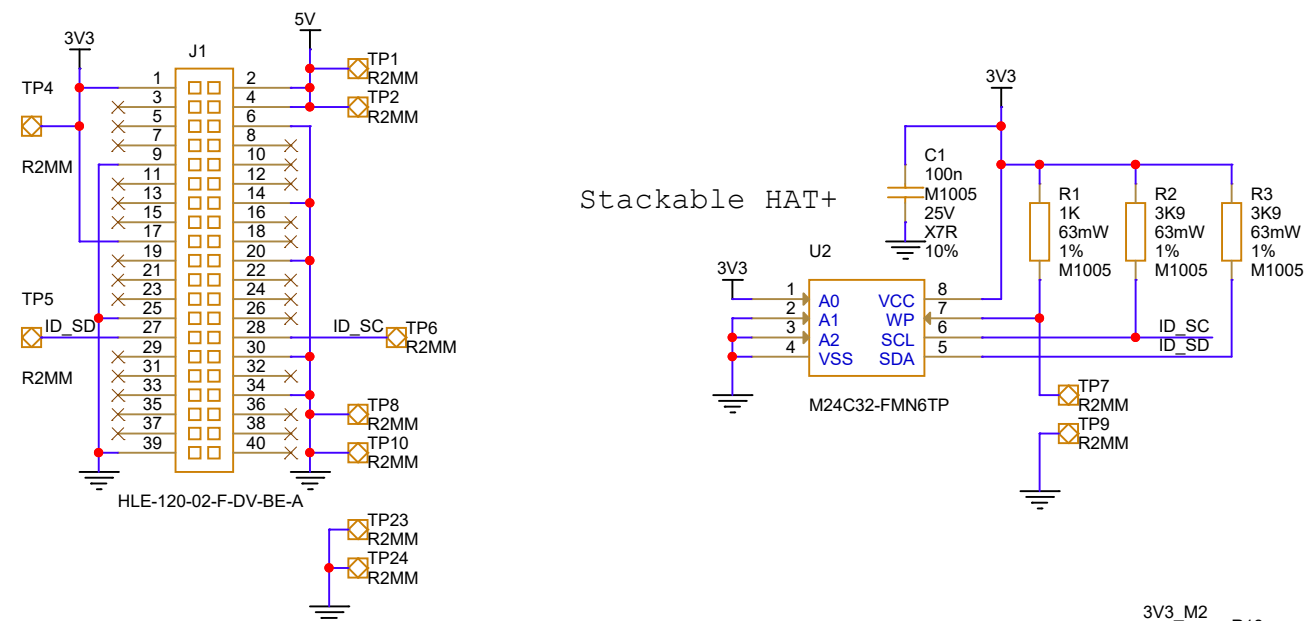
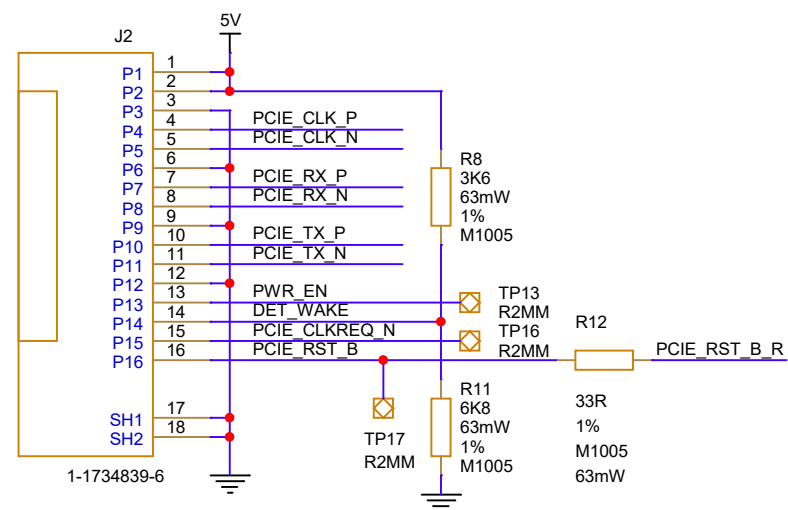


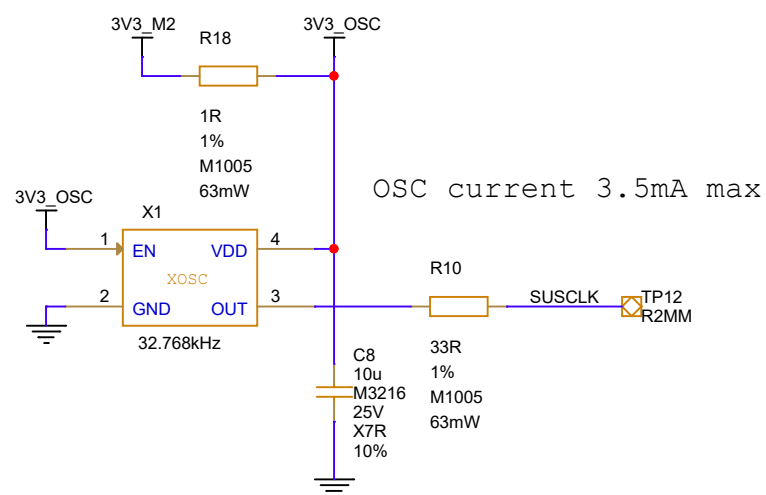
D



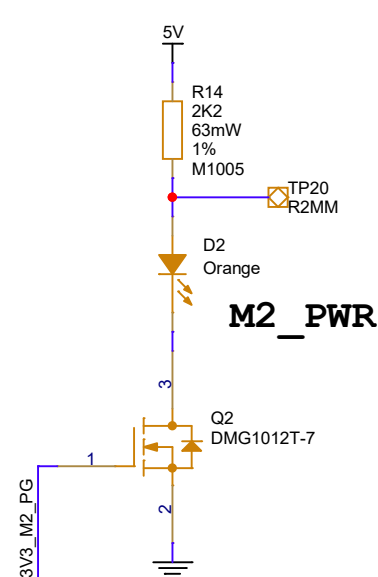
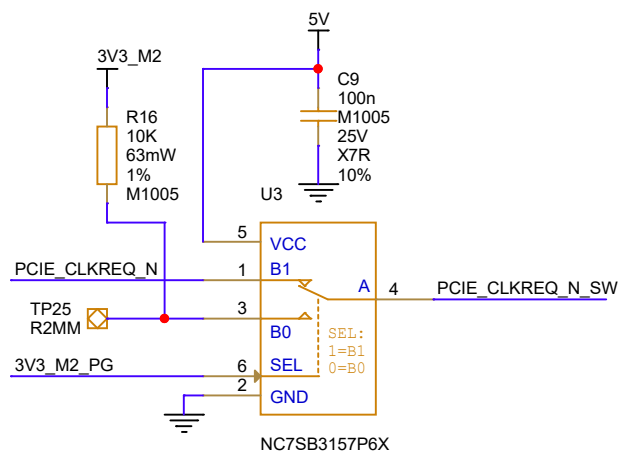
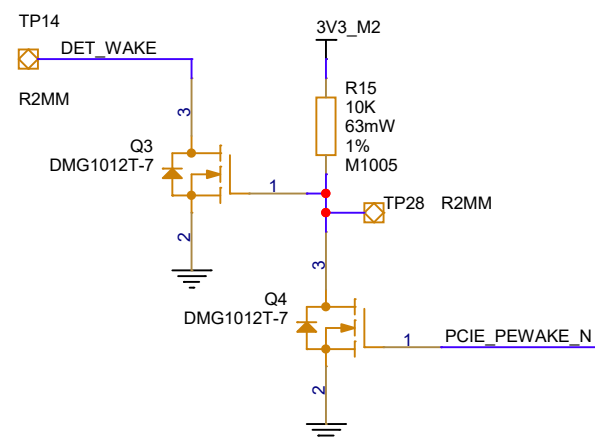
TOP CONTACT



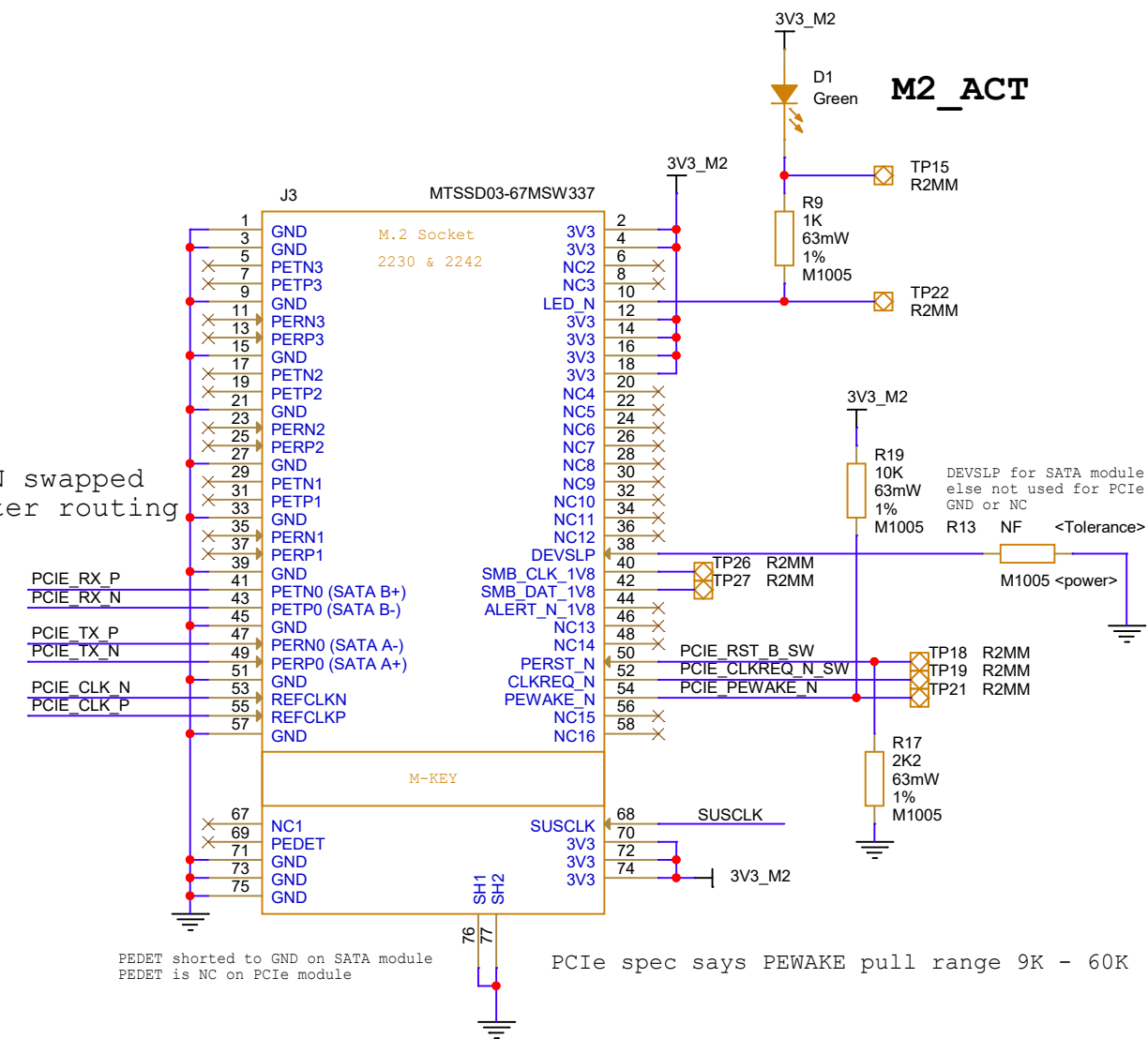
C

NOTE P/N swapped
for better routing

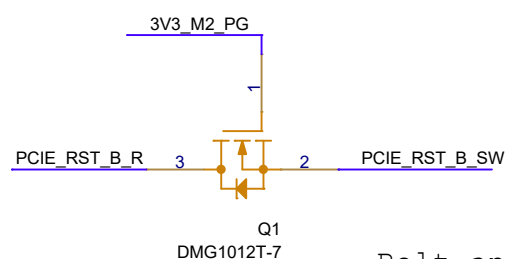
B



M2_PWR



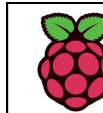
A

Belt and braces isolate CLKREQ and
RESET when M2 power off

PERST_N: PCIe Reset. When asserted low the device performs a reset.

CLKREQ_N: Request to run/stop ref clk (also used for link power state signalling)

PEWAKE_N: Asserted low by device to wakeup PCIe host



Raspberry Pi

© Raspberry Pi 2023

www.raspberrypi.com

Title		Raspberry Pi M.2 M-Key HAT+		Drawn By		James Adams	
Size	A3	Ref	RPI-M2MKEY-HAT	Rev	3		
Date:	Friday, January 05, 2024			Sheet	1	of	1